Eulerian circuits and path decompositions in quartic planar graphs

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Eulerian circuits

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- (Le, 2019) High minimum degree is sufficient to guarantee a k-lsa EC

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- ℓ -Isa Eulerian circuit + divisibility guarantees a $P_{\ell+1}$ -decomposition

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Corollary. Every connected quartic planar of even order that does not contain F_6 admits a P_5 -decomposition.

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Corollary. A connected quartic planar graph admits a P_5 -decomposition iff it has even order.

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abwevyxcbdafyexwvdfca

 $abcy_{n-2} \dots y_2 y_1 x_1 x_2 \dots x_{n-1} ca x_1 b y_1 x_2 y_2 x_3 \dots x_{n-2} y_{n-2} x_{n-1} a$

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